ABB ABB Series Service Manual

07 SL 97

1. Description

ABB Central Unit 07 SL 97

3. Product Information

ABB Central Unit 07 SL 97

Communication: Includes CS 31 - Field bus.

Memory:

Program Memory: 480 kByte Flash EPROM and RAM.

Plug-in Memory: Smart Media Card (Flash) for data storage and reloading the SPS-program.

Data Memory: 8192 bits, 8192 words (16 bit), and 1024 double words (32 bit).

Variables: 256 k Variables, including 16 k RETAIN.

Performance: Cycle time for 1 Kbyte is 0.22 ms.

Inputs/Outputs:

Maximum DI/DO: 992 in total.

Maximum AI/AO: 224 AI and 224 AO.

Execution & Control:

Programming System: 907AC1131.

Program Execution: Cyclic, time controlled Multitasking (20 Tasks).

Logic Components: Unrestricted Timers and Counters; 256 Sequencers (of 16 sequences).

Special Functions: Protocols such as ARCNET®, MODBUS®; integrated Profibus DP or DeviceNet as optional communication processors; PI and PID controllers; 32 bit arithmetic; ASCII communication.

Hardware Details: Password protection, exchangeable battery, real-time clock, and withdrawable terminal blocks with screw terminals.

Mounting: Placed in PCI-Slot (fullsize).