GE GE □□□ Series Service Manual

IS200ERIOH1A

1. Description

SPECIFICATION

2. Specifications

Specification	Value
Manufacturer	**General Electric (GE)**
Model	**Number IS200ERIOH1A**
Manufacturing	**Location Salem, VA, USA**
Product	**Type Exciter Regulator I/O**
Applications	**Redundant and Simplex**
About	**the IS200ERIOH1A**
Produced	**by General Electric for the EX2100 Excitation Regulator and the Mark VI series as a whole, the IS200ERIOH1A board offers the I/O interface for customer and system I/O. It provides these functions for Regulator Control systems both simplex and redundant. For simplex applications, the I/O is mounted in the ERBP (Exciter Regulator Backplane) and interfaces customer I/O signals for the ECTBG2 terminal board and the EPCT terminal board. The IS200ERIOH1A is also capable of interfacing system I/O signals for the EROC (Exciter Regulator Options Card) and ERDD (Exciter Regulator Interface and Dynamic Discharge). For redudant applications, it also interfaces system I/O signals for the ERRR (Exciter Regulator Redundant Relay).**

3. Product Information

SPECIFICATION

Part Number: IS200ERIOH1A

Manufacturer: General Electric

Series: Mark VI

Function: Ground Detector Board

Within the EX2100 system, the IS200ERIOH1A serves as an Exciter Regulator I/O board. In the EX2100 Regulator Control systems, the IS200ERIOH1A offers a system and customer I/O. (both simplex and redundant.) Depending on the type of system, the board is mounted in different locations. In simplex applications, for example, the board is mounted in ERBP. Three IS200ERIOH1A are required in redundant applications. These attach to the ERRB M2 and C, as well as the ERBP M1. The front faceplate of the IS200ERIOH1A is double wide and has various components affixed to it.

One toggle switch is included in this package. Two backplane connectors (P1/P2) are located on the board's back edge. The board includes two CPU sockets as well as a field-programmable gate array (FPGA) for data collecting and storing into DPM control.

When working with simplex systems, this board also provides an interface system I/O from the ERO and ERDD boards. The board is mounted to the ERBP M1 and the ERRB M2/C in a redundant configuration. The M1/M2/C controllers are all on the same PCB.

A front faceplate with many mounted components, including a toggle switch, is featured on this board. It also features two backplane connectors on the back edge, hundreds of capacitors and resistors on the surface, and several integrated circuits. Two CPU sockets and one field-programmable gate array are included in these circuits.

IS200ERIOH1A Technical Specifications

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Product Type Exciter Regulator I/O

Applications Redundant and Simplex

About the IS200ERIOH1A

Produced by General Electric for the EX2100 Excitation Regulator and the Mark VI series as a whole, the IS200ERIOH1A board offers the I/O interface for customer and system I/O. It provides these functions for Regulator Control systems both simplex and redundant. For simplex applications, the I/O is mounted in the ERBP (Exciter Regulator Backplane) and interfaces customer I/O signals for the ECTBG2 terminal board and the EPCT terminal board. The IS200ERIOH1A is also capable of interfacing system I/O signals for the EROC (Exciter Regulator Options Card) and ERDD (Exciter Regulator Interface and Dynamic Discharge). For redudant applications, it also interfaces system I/O signals for the ERRR (Exciter Regulator Redundant Relay).

The Exciter Regulator I/O is connected to the EROC and ERDD boards by means of the ERBP or ERRB backplanes, the former of which is for simplex applications and the latter of which is for redundant applications. Additionally, the ERIO is connected to the EPCT and ECTB through two twenty-five pin connectors to the ERBP and the ERRB. The FPGA (Field Programmable Gate Array) controls storage and data acquisition into DPM (Dual Port Memory). This particular unit is a revision of the original part, indicated by the "A" at the end of the model number.